

Course

ESL - Design of Circuit Board Layout

Version: 0 | Last Change: - | Draft: 0 | Status: Entwurf

^ General information

Long name	Design of Circuit Board Layout
Approving CModule	ESL_BaET
Responsible	Studiengangsleiter(in) Bachelor Elektrotechnik
Level	Bachelor
Semester in the year	summer semester
Duration	Semester
Hours in self-study	null
ECTS	5
Professors	Nomen nominandum (NN) Platzhalter
Requirements	Elektronik 1 Grundgebiete der Elektrotechnik 1,2 Physik1,2 Elektronik 2
Language	German
Separate final exam	Yes

Final exam

Details

Minimum standard

null

^ Lecture / Exercises

Learning goals

Knowledge

creation of layouts with Ultiboard
transfer of the circuits from Multisim to Ultiboard
choosing of the device body
choosing of the circuit board layer
positioning of the devices
transfer of the conducting path
by hand
automatically

Skills

design of layouts by yourself

komplexe Aufgaben im Team bewältigen
Übungen
Absprachen und Termine einhalten

Expenditure classroom teaching

Type	Attendance (h/Wk.)
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Separate exam

Exam Type

solving exercises within limited functional / methodical scope

Details

unbenotet

Minimum standard

null

^ Practical training

Learning goals

Knowledge

development of layouts

operating with Ultiboard

Skills

realization of layouts

usage of the software Ultiboard

komplexe Aufgaben im Team bewältigen

build-up simple layouts

Absprachen und Termine einhalten

Reviews planen und durchführen

Expenditure classroom teaching

Type

Attendance (h/Wk.)

Separate exam

Exam Type

solving exercises within limited functional / methodical scope

Details

unbenotet

Minimum standard

null

^ Project

Learning goals

Knowledge

Skills

Expenditure classroom teaching

Type	Attendance (h/Wk.)
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Separate exam

Exam Type

solving exercises within limited functional / methodical scope

Details

null

Minimum standard

null